cmp r4, r5 # compare regs. 4 & 5, and set condition code
be lab1 # branch to lab1 if cond. code specifies equal
mov r3, 0 # place a zero in register 3
lab1: ...program continues at this point

Figure 5.11 An example of using a condition code. An ALU operation sets the condition code, and a later *conditional branch* instruction tests the condition code.