

Instr.	ALU			OP ₁	OP ₂	RES ₁	RES ₂	REG. INTERFACE					
1	0	0	0	1	0	0	0	0	1	0	1	0	0
2	0	0	0	0	1	0	0	0	1	1	1	0	1
3	0	0	1	0	0	0	0	0	0	0	0	0	0
4	0	0	0	0	0	0	1	1	0	0	1	0	0

Figure 7.9 An example horizontal microcode program that consists of four instructions of thirteen bits per instruction. Each instruction corresponds to a step listed in Figure 7.8.